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ATTORNEY DOCKET NO.	CONFIRMATION NO.

FIRST NAMED INVENTOR APPLICATION NO. FILING DATE 10/625,688 07/24/2003 Masashi Hiratsuka 116662 9932 **EXAMINER** 25944 7590 02/04/2005 OLIFF & BERRIDGE, PLC STERRETT, JEFFREY L P.O. BOX 19928 ART UNIT PAPER NUMBER ALEXANDRIA, VA 22320

DATE MAILED: 02/04/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

			CYN		
	Application No.	Applicant(s)	(3.		
Office Antique Commence	10/625,688	HIRATSUKA ET AL.			
Office Action Summary	Examiner	Art Unit			
	Jeffrey L. Sterrett	2838			
The MAILING DATE of this communication app Period for Reply	pears on the cover sheet w	ith the correspondence addres	SS		
A SHORTENED STATUTORY PERIOD FOR REPL' THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.1: after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply - If NO period for reply is specified above, the maximum statutory period v - Failure to reply within the set or extended period for reply will, by statute Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a y within the statutory minimum of thi vill apply and will expire SIX (6) MOI , cause the application to become A	reply be timely filed rty (30) days will be considered timely. NTHS from the mailing date of this commu. BANDONED (35 U.S.C. & 133).	inication.		
Status	•				
1) Responsive to communication(s) filed on					
	action is non-final.				
3) Since this application is in condition for allowar	· —				
closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.					
Disposition of Claims					
4) Claim(s) 1-14 is/are pending in the application.	. ·				
4a) Of the above claim(s) is/are withdray					
5) Claim(s) is/are allowed.					
6)⊠ Claim(s) <u>1-14</u> is/are rejected.					
7) Claim(s) is/are objected to.					
8) Claim(s) are subject to restriction and/o	r election requirement.	•			
Application Papers					
9)☐ The specification is objected to by the Examiner.					
10)⊠ The drawing(s) filed on <u>24 July 2003</u> is/are: a)⊠ accepted or b)⊡ objected to by the Examiner.					
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).					
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).					
11)☐ The oath or declaration is objected to by the Ex	caminer. Note the attache	d Office Action or form PTO-1	52.		
Priority under 35 U.S.C. § 119					
12)⊠ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).					
a) ⊠ All b) □ Some * c) □ None of:					
1.⊠ Certified copies of the priority documents have been received.					
2. Certified copies of the priority documents	s have been received in A	Application No			
3. Copies of the certified copies of the prior	rity documents have beer	received in this National Sta	ge		
application from the International Bureau	, , , , , , , , , , , , , , , , , , , ,				
* See the attached detailed Office action for a list	of the certified copies not	received.			
•·· • · · · · · · · · · · · · · · · · ·					
Attachment(s) 1) Notice of References Cited (PTO-892)	4) Intervious	Summary (PTO-413)			
2) Notice of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail Date			
3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date 7/24/03.	5)	nformal Patent Application (PTO-152	2)		
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Receipt is acknowledged of the foreign priority papers submitted under 35
 U.S.C. 119(a)-(d), which foreign priority papers have been placed of record in the file.

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 3. Claims 1-5 and 10 are rejected under 35 U.S.C. 102(b) as being anticipated by Sakakibara et al (US 4,679,129).

Sakakibara et al discloses a power supply (figure 8) comprising first (23a) and second (23b) parallel resonance circuits, first (11) and second (18) transistors respectively connected to the first and second resonance circuits forming first and second parallel resonance circuit parts, and a series resonance circuit (26) having one end connected (via DC source 12) to "an end" of <u>the first parallel resonance circuit</u> <u>part</u> and the other end connected (via primary winding n1) to "an end" of <u>the second</u> <u>parallel resonance circuit part</u> wherein the first and second transistors are alternatively switched (i.e. out of phase with each other).

- 4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 5. Claims 6, 7, 11, and 12 are rejected under 35 U.S.C. 103(a) as being unpatentable over Sakakibara et al.

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Sakakibara et al discloses a power supply as explained above and as recited by claims 6, 7, 11, and 12 except for utilizing current limiting resistors. Official notice is taken that current limiting resistors were notoriously old and known expedients in the art at the time of the invention. It would have been obvious to one of ordinary skill in the art at the time of the invention to modified the power supply of Sakakibara et al by utilizing current limiting resistors in order to limit the current through the transistors to a safe or desired level.

6. Claim 8 is rejected under 35 U.S.C. 103(a) as being unpatentable over Sakakibara et al in view of Jacobson et al (US 5,151,852).

Sakakibara et al discloses a power supply as explained above and as recited by claim 8 except for providing the output of the power supply at a node between the capacitor and inductor of the series resonance circuit. Jacobson et al discloses a power supply (12) old and known in the art at the time of the invention where the output of the power supply is provided at a node between the capacitor (C3) and inductor (L5) of the series resonance circuit (20). It would have been obvious to one of ordinary skill in the art at the time of the invention to modified the power supply of Sakakibara et al by providing the output of the power supply at a node between the capacitor and inductor of the series resonance circuit as taught by Jacobson et al in order to derive a desired output waveform.

7. Claims 9 and 13 are rejected under 35 U.S.C. 103(a) as being unpatentable over Sakakibara et al.

Sakakibara et al discloses a power supply as explained above and as recited by claims 9 and 13 except for utilizing a voltage doubler rectifier circuit. Official notice is taken that voltage doubler rectifier circuits were notoriously old and known expedients in the art at the time of the invention. It would have been obvious to one of ordinary skill in the art at the time of the invention to modified the power supply of Sakakibara et al by utilizing a voltage doubler rectifier circuit on the output of the power supply in order to boost the output voltage to a desired level.

8. Claim 14 is rejected under 35 U.S.C. 103(a) as being unpatentable over Sakakibara et al.

Sakakibara et al discloses a power supply as explained above and as recited by claim 14 except for utilizing the power supply to provide power to an image forming apparatus. Official notice is taken that utilizing a power supply to provide power to an image forming apparatus was an notoriously old and known expedient in the art at the time of the invention. It would have been obvious to one of ordinary skill in the art at the time of the invention to utilized the power supply of Sakakibara et al to provide power to an image forming apparatus since all electrical devices require a power supply of some kind.

9. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Tellert (US 4,355,243), Jain (US 5,159,541), Rohrbeck et al (US 5,586,017), Bhagwat et al (US 5,875,103), and Shiizu et al (US 5,946,206) are cited to show power supplies old and known in the art at the time of the invention.

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10. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jeffrey L. Sterrett whose telephone number is (571) 272-2085. The examiner can normally be reached on Monday-Thursday & 8:00am-5:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Michael Sherry can be reached on (571) 272-2084. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Jeffrey L. Sterrett
Primary Examiner
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August August

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